

**VISHWAKARMA UNIVERSITY**  
**FACULTY PROFILE**

Sr. No.	Details
1	Introduction – <i>My inquisitiveness for electronic circuit boards led me to opt for “Electronics and Telecommunication Engineering” for under graduate degree program from Savitribai Phule Pune University, Pune, Maharashtra, India (SPPU, formerly University of Pune). I worked consistently to achieve First Class in under graduate. Further, I completed “Master of Engineering in Electronics and Telecommunication (VLSI and Embedded system)” with First Class grade from same university. I had successfully demonstrated project which was embedded system based and extended version of my UG project named as “Travolution-System for Road Safety”. So I am accomplished, skilled and ambitious person with diverse knowledge in the areas of Electronics and communication engineering.</i>
2	Name : <i>Prof. Subodh Babasaheb Kharat</i> Qualification : <i>B.E.(EnTC), M.E. (VLSI and Embedded Systems)</i>
3	Teaching Philosophy: <i>My ability is to generate effective teaching by making use of various methodologies and to teach in a multi-ethnic and multicultural environment which can possess strong knowledge to students on subject taught for electronics.</i>
4	Core Teaching Areas : <i>Basic Electronics Engineering, Basic Electrical Engineering, Digital Electronics</i>
5	Experience (as on 31 <sup>st</sup> Dec. 2018) putting industry and teaching together: <i>1.5Years</i>
6	Areas of Interests (Research): <i>VLSI Design, Embedded Systems, Mechatronics.</i>
7	Doctoral Research Scholars :
8	Funded Research Projects
9	Training / Consultancy / Projects etc.
10	Awards & Achievements
11	Professional Memberships
12	Learning and Development Initiatives (FDPs, MDPs, MOOCs etc.) -
13	Global Exposure

