

**Faculty of Science and Technology**

**Programme Structure**

For

First Year

**Bachelor of Technology (Computer Science and Engineering- Artificial Intelligence and Machine Learning)**

Programme Code: BTECAM

Pattern 2024

With Effect from Academic Year 2025 – 2026

2. Title: Artificial Intelligence and Machine Learning Programme Structure Form No: IQAC-101

SEM-I

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| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Faculty** | | ***Science and Technology*** | | | | **Pattern** | | | | | 2024 | | |
| **Department** | | **Engineering Sciences** | | | | **Date (w.e.f.)** | | | | |  | | |
| **Programme** | | **B.Tech (CSE- Artificial Intelligence and Machine Learning) (BTECAM)** | | | |  | | | | |  | | |
| **Course Type** | **Course Code** | | **Course Name** | **Teaching Scheme**  **(Hours/Week)** | | | | **Credit** | **Examination Scheme and Marks** | | | |
|  |  | |  | **L** | **T** | | **P** | **C** | **CIE** | **ESE** | | **Total** |
| BSC | BTECAM24101 | | Applied Mathematics | 3 | 1 | | 0 | 4 | 60 | 40 | | 100 |
| ESC | BTECAM24102 | | Fundamental of Electronics | 3 | 0 | | 0 | 3 | 60 | 40 | | 100 |
| BSC | BTECAM24103 | | Computer Organization | 2 | 0 | | 0 | 2 | 50 | 00 | | 50 |
| ESC | BTECAM24104 | | Introduction to Computer Programming | 3 | 0 | | 0 | 3 | 60 | 40 | | 100 |
| ESC | BTECAM24105 | | Fundamental of Electronics Lab | 0 | 0 | | 2 | 1 | 15 | 10 | | 25 |
| ESC | BTECAM24106 | | Introduction to Computer Programming Lab | 0 | 0 | | 2 | 1 | 15 | 10 | | 25 |
| VSEC | BTECAM24107 | | Web Technology Lab | 1 | 0 | | 2 | 2 | 30 | 20 | | 50 |
| CC | BTECAM24108 | | Capstone Project - I | 0 | 0 | | 4 | 2 | 50 | 00 | | 50 |
|  | BTECAM24109 | | Induction Training |  |  | | 1\* | 0 |  |  | | 0 |
| IKS | IKS24AI01 | | Indian Knowledge System Concepts and Applications in Engineering | 2 | 0 | | 0 | 2 | 50 | 00 | | 50 |
|  |  | | Total | 16 | 01 | | 06 | 20 | 390 | 160 | | 550 |
| **Instructions, if any: 1 Theory/Tutorial Hour = 1 Credit, 2 Practical hours = 1 Credit** | | | | | | | | | | | | |

SEM-II

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| **Faculty** | | ***Science and Technology*** | | | | **Pattern (AY-2024- 2025)** | | | | | 2024 | |
| **Department** | | **Engineering Sciences** | | | | **Date (w.e.f.)** | | | | |  | |
| **Programme** | | **B.Tech (CSE- Artificial Intelligence and Machine Learning) (BTECAM)** | | | |  | | | | |  | |
| **Course Type** | **Course Code** | | **Course Name** | **Teaching Scheme**  **(Hours/Week)** | | | | **Credit** | **Examination Scheme and Marks** | | | |
|  |  | |  | **L** | **T** | | **P** | **C** | **CIE** | **ESE** | | **Total** |
| ESC | BTECAM24201 | | Prompt Engineering | 2 | 0 | | 0 | 2 | 50 | 00 | | 50 |
| BSC | BTECAM24202 | | Linear Algebra and  Statistics | 3 | 1 | | 0 | 4 | 60 | 40 | | 100 |
| BSC | BTECAM24203 | | Processor Architecture | 3 | 0 | | 0 | 3 | 60 | 40 | | 100 |
| PCC | BTECAM24204 | | Logic  Development and Programming | 2 | 0 | | 0 | 2 | 30 | 20 | | 50 |
| PCC | BTECAM24205 | | Logic Development and  Programming Lab | 0 | 0 | | 2 | 1 | 15 | 10 | | 25 |
| BSC | BTECAM24206 | | Processor Architecture Lab | 0 | 0 | | 2 | 1 | 15 | 10 | | 25 |
| VSEC | BTECAM24207 | | Introduction to Microcontroller programming Lab | 1 | 0 | | 2 | 2 | 30 | 20 | | 50 |
| ESC | BTECAM24208 | | Technical Skill development Lab-1  (OOP using C++) | 0 | 0 | | 2 | 1 | 15 | 10 | | 25 |
| CC | BTECAM24209 | | Capstone Project - II | 0 | 0 | | 4 | 2 | 50 | 00 | | 50 |
| AEC | AE24AI01 | | Corporate Communication | 1 | 1 | | 0 | 2 | 50 | 00 | | 50 |
|  |  | | Total | 12 | 04 | | 08 | 20 | 375 | 150 | | 525 |
| **Instructions, if any: 1 Theory/Tutorial Hour = 1 Credit, 2 Practical hours = 1 Credit** | | | | | | | | | | | | |

Head of the Department Dean

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ERP to provide document upload facility for evidencing – BoS meeting MoM, AC MoM, final approved grid with BoS chairman signature, etc.